



SplashBase LM3S6x - Development Kit

Thank you for purchasing The SplashBase LM3S6x Development Kit.

Overview

The SplashBase development kit is a powerful platform enabling you to create network enabled devices. Also utilising it's stacking header system multiple SolderBridges can be used to expand it's capabilities.

Technical Specification

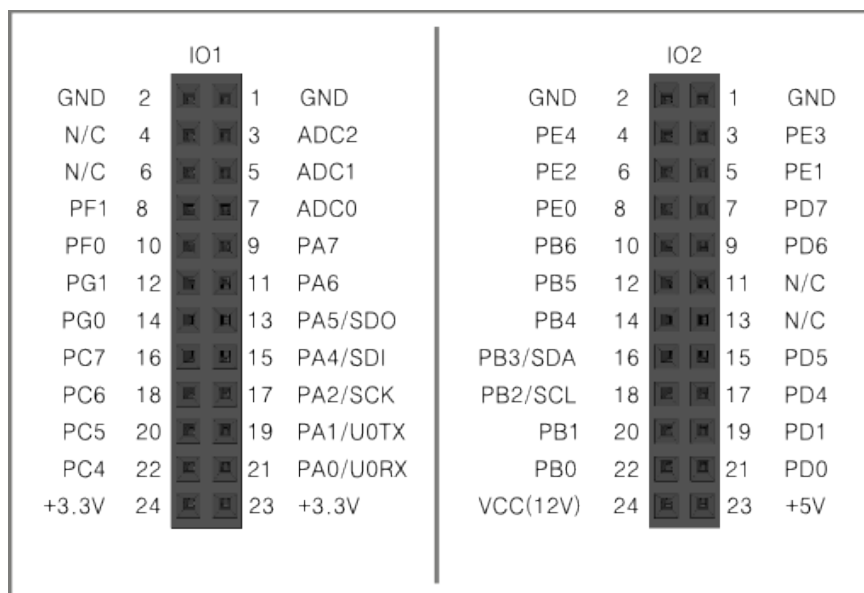
- USB CDC (Serial port)
- 100/10Mbit Ethernet
- DC Power socket 5v - 18v (Please only use 12v if the Relay SolderBridge is connected)
- Current consumption : 130mA (No Network) – 165mA (Ethernet Connected) at 5v
- ARM Cortex M3 50Mhz – TI LM3S6x
- Ethernet Boot-loader for code updates
- SPI Eeprom with unique MAC address
- Temperature Sensor

The SplashBase power circuit is flexible and allows both USB and 12v DC at the same time and will operate off either

Reference	Purpose
IO1	Stacking Header for expansion
IO2	Stacking Header for expansion
LED1 / ACT	Ethernet activity
LED2 / LINK	Network link detected
LED3	Power Indicator
S1	Reset push button
S2	Boot push button
J2	3 Channel PWM output. RGB Led Control
J4	Uart0 – TTL 3.3v
J1	Jtag header for device reprogramming/debugging
J6	USB – Serial CDC Connected to Uart1
J7	Ethernet Socket
J3	DC Power jack, centre positive

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The pin out of the SplashBase Expansion connectors IO1 and IO2 is as follows.



Each of the connections on the Expansion IO connectors may have one or more functions or be used by the base board. This table details each connectors pin out and will indicate if jumpers can be removed to free that Port/pin for your own use. Where there is no description it indicates the pin is not used when the SplashBase is standalone.

Base Board Pin No	SplashBase LM3S Function	Notes
IO1		
1	GND	
2	GND	
3	ADC2	0-3.3v Analogue input
4	N/C	
5	ADC1	0-3.3v Analogue input
6	N/C	
7	ADC0	By Default SJ1 connects on board Thermistor. 0-3.3v Analogue input
8	PortF Pin1	
9	PortA Pin7	
10	PortF Pin0	
11	PortA Pin6/ CPP1	By Default SJ4 connects PWM output to J2 pin 2 – Green Channel
12	PortG Pin1	
13	SDO	SPI Bus connected to Eeprom, can be shared externally using another Chip select
14	PortG Pin0	
15	SDI	SPI Bus connected to Eeprom, can be shared externally using another Chip select

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16	PortC Pin7	
17	SCK	SPI Bus connected to Eeprom, can be shared externally using another Chip select
18	PortC Pin6 / CCP3	
19	U0TX	Also Available on J4
20	PortC Pin5	
21	U0RX	Also Available on J4
22	PortC Pin4	
23	3.3V	
24	3.3V	
IO2		
1	GND	
2	GND	
3	PortE Pin3	
4	PortE Pin4	
5	PortE Pin1	
6	PortE Pin2	
7	PortD Pin7	
8	PortE Pin0	
9	PortD Pin6	
10	PortB Pin6	
11	N/C	
12	PortB Pin5	
13	N/C	
14	PortB Pin4	By Default SJ2 connects Boot Push Button
15	PortD Pin5	
16	SDA / PortB Pin3	Has an external pull up on the base board. Generally used for I2C communication can be used as a GPIO
17	PortD Pin4	
18	SCL / PortB Pin2	Has an external pull up on the base board. Generally used for I2C communication can be used as a GPIO
19	PortD Pin1/ PWM0	
20	PortB Pin1 / CCP2	By Default SJ5 connects PWM output to J2 pin 3 – Blue Channel
21	PortD Pin0/ PWM1	
22	PortB Pin0 / CCP0	By Default SJ3 connects PWM output to J2 pin1 – Red Channel
23	+5V	Present if power is supplied by USB
24	VCC (12V)	This is the voltage connected to the DC Input of the Base board

For support or any questions you may have see our forum : <http://forum.soldersplash.co.uk>

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FCC Warning

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